



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Mark W. Miles
Serial No. : 09/413,222
Filed : October 5, 1999
Title : PHOTONIC MEMS AND STRUCTURES

Art Unit : 2873
Examiner : H. Dang

Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO ACTION DATED FEBRUARY 27, 2001

Please amend claim 5 as follows:

1. 5. (amended) A line-at-a-time electronic driving method comprising
applying a bias voltage to rows ~~or columns~~ of a device,
applying data voltages to the columns ~~or rows~~ alternately about a value of the bias
voltage,

actuation of the device occurring when a difference between the values of the data
voltage and a select voltage is above a first predetermined level,

release of the device occurring when the difference between the values of the data voltage
and the select voltage is below a second predetermined level lowest, and
the device maintaining its state when the select voltage is at the bias level.

Add claims 52 through 59:

3. 52. The method of claim 5 in which the bias voltage is within a hysteresis window of
the device.

4. 53. The method of claim 52 in which the bias voltage is in the middle of the
hysteresis window.

54. The method of claim 51 in which the first predetermined threshold is about 9 volts
and the second predetermined threshold is about 0 volts.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being
deposited with the United States Postal Service as first class mail with
sufficient postage on the date indicated below and is addressed to the
Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit

May 23, 2001

Signature

Lesley J. Arcidy
Typed or Printed Name of Person Signing Certificate

05/23/2001 RABABII 00000003 061050 09413222

0. 00 00

#814/501
6-501
R. Grader
RECEIVED
MAY 31 2001
TC 2800 MAIL ROOM